

U.S. Department of Commerce, Patent and Trademark Office						Atty Docket No.		Serial No.	
						CIS0069US		09/822,950	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Applicant(s)			
(Use several sheets if necessary)						Andrew J. Thurston			
MAR 05 2003						Filing Date		Group	
						March 30, 2001		2184	
U.S. Patent Documents									
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA								
	AB								
	AC								
	AD								
	AE								
	AF								
	AG								
	AH								
	AI								
	AJ								
	AK								
Foreign Patent Documents									
							Translation		
		Document	Date	Country	Class	Subclass	Yes	No	
	AL								
	AM								
	AN								
	AO								
	AP								
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
DG	AQ	Christian Schuler, "Code Generation Tools For Hardware Implementation Of FEC Circuits," GMD FOKUS, Kaiserin-Augusta-Allee 31, 10589 Berlin, Germany.							
DG	AR	Shu Lin and Daniel J. Costello, Jr., "Error Control Coding <i>Fundamentals and Applications</i> ," Prentice-Hall, Inc. Englewood Cliffs, New Jersey, Chapter 6, pp. 141-183.							
	AS								
Examiner		Date Considered							
DG		8/18/04							
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.									